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<u>L29</u>	L28 and (reuse\$ or reusable\$)	35	<u>L29</u>
<u>L28</u>	architectur\$ and (many\$ near5 one\$ near5 map\$)	215	<u>L28</u>
<u>L27</u>	L26 and dimension\$	1	<u>L27</u>
<u>L26</u>	L25 and map\$	1	<u>L26</u>
<u>L25</u>	6094716.pn.	1	<u>L25</u>
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<u>L23</u>	6338134.pn.	1	<u>L23</u>
<u>L22</u>	reus\$ near8 (instruction\$ or compute or computation\$) and vector\$ and symbol\$ and (sequence near5 execution\$)	26	<u>L22</u>
<u>L21</u>	reuse near5 (instruction\$) and computation\$	116	<u>L21</u>
<u>L20</u>	L19 and ((reus\$ near5 instruction\$) or (reus\$ near5 comput\$))	57	<u>L20</u>
<u>L19</u>	value\$ and register\$ and input near5 operand and (reuse\$ or reusable\$)	258	<u>L19</u>
<u>L18</u>	l9 and duplicat\$	0	<u>L18</u>
<u>L17</u>	l9 and (repeat\$ or recurrent\$ or redun\$)	0	<u>L17</u>
<u>L16</u>	l9 and (ascend\$ or greater\$)	0	<u>L16</u>
<u>L15</u>	l9 and integer\$	1	<u>L15</u>
<u>L14</u>	l9 and (symbol\$ or vector\$ or text\$)	0	<u>L14</u>
<u>L13</u>	l9 and (text\$ or phras\$)	0	<u>L13</u>
<u>L12</u>	l9 and map\$	1	<u>L12</u>
<u>L11</u>	l9 and reuse\$	1	<u>L11</u>
<u>L10</u>	L9 and resua\$	0	<u>L10</u>
<u>L9</u>	6505293.pn.	1	<u>L9</u>
<u>L8</u>	l1 and (reus\$ near8 comput\$)	3	<u>L8</u>
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<u>L4</u>	L3 and vector\$	80	<u>L4</u>
<u>L3</u>	l1 and (phras\$ or text\$)	103	<u>L3</u>
<u>L2</u>	L1 and (live-in or live in)	0	<u>L2</u>
<u>L1</u>	(instruction\$ near7 reus\$) and map\$ and execut\$ and computation\$	156	<u>L1</u>

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|----------|---|-----|
| 1 | Computing curricula 2001 | 80% |
| | Journal of Educational Resources in Computing (JERIC) September 2001 | |
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| 2 | Reconsidering custom memory allocation | 77% |
| | Emery D. Berger , Benjamin G. Zorn , Kathryn S. McKinley
ACM SIGPLAN Notices , Proceedings of the 17th ACM conference on
Object-oriented programming, systems, languages, and applications
November 2002
Volume 37 Issue 11
Programmers hoping to achieve performance improvements often
use custom memory allocators. This in-depth study examines eight
applications that use custom allocators. Surprisingly, for six of these
applications, a state-of-the-art general-purpose allocator (the Lea
allocator) performs as well as or better than the custom allocators.
The two exceptions use regions, which deliver higher performance
(improvements of up to 44%). Regions also reduce programmer
burden and eliminate a source of memory ... | |
|
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| 3 | Better exploration of region-level value locality with integrated
computation reuse and value prediction | 77% |
| | Youfeng Wu , Dong-Yuan Chen , Jesse Fang
ACM SIGARCH Computer Architecture News , Proceedings of the 28th
annual international symposium on on Computer architecture May 2001
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